



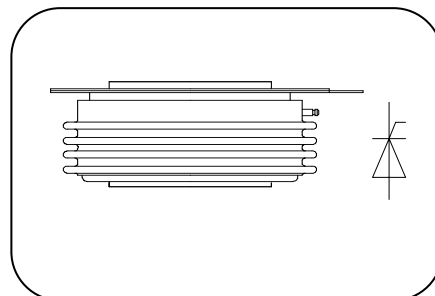
Features

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

$I_{T(AV)}$ **8000 A**
 V_{DRM}/V_{RRM} **2400-3000V**
 I_{TSM} **115 kA**
 I^2t **66000 10³A²S**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_J(^{\circ}C)$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled, $T_C=70^{\circ}C$	115			8000	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$t_p=10ms$	115	2400		3000	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	25			10	mA
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	115			600	mA
I_{TSM}	Surge on-state current	10ms half sine wave	115			115	kA
I^2t	I^2t for fusing coordination	$V_R=0.6V_{RRM}$				66000	A ² s*10 ³
V_{TO}	Threshold voltage		115			0.88	V
r_T	On-state slope resistance					0.12	mΩ
V_{TM}	Peak on-state voltage	$I_{TM}=6000A$, $F=180kN$	25			1.60	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	115			1000	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 3000A, Gate pulse $t_r \leq 0.5\mu s$ $IGM=1.5A$	115			500	A/μs
Q_{rr}	Recovery charge	$I_{TM}=3000A$, $t_p=1000\mu s$, $di/dt=-5A/\mu s$, $V_R=100V$	115		8800		μC
I_{GT}	Gate trigger current	$V_A=12V$, $I_A=1A$	25	40		300	mA
V_{GT}	Gate trigger voltage			0.8		3.0	V
I_H	Holding current			25		400	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	115			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 180.0kN				0.002	°C /W
$R_{th(c-hs)}$	Thermal resistance case to heatsink					0.0005	°C /W
F_m	Mounting force			162	180	198	kN
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight				5270		g
Outline	KT155dT						

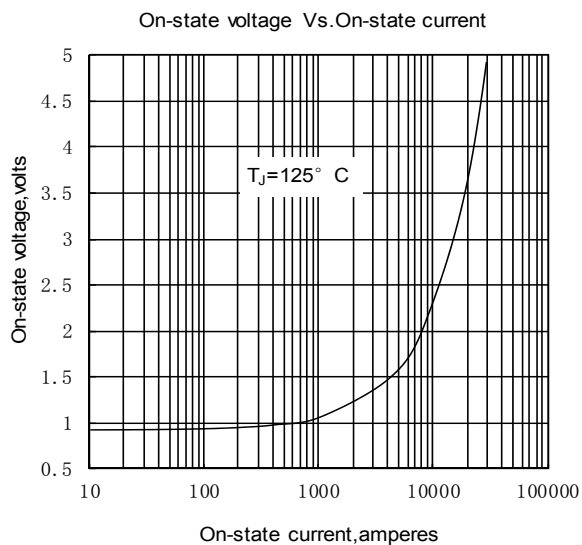


Fig.1

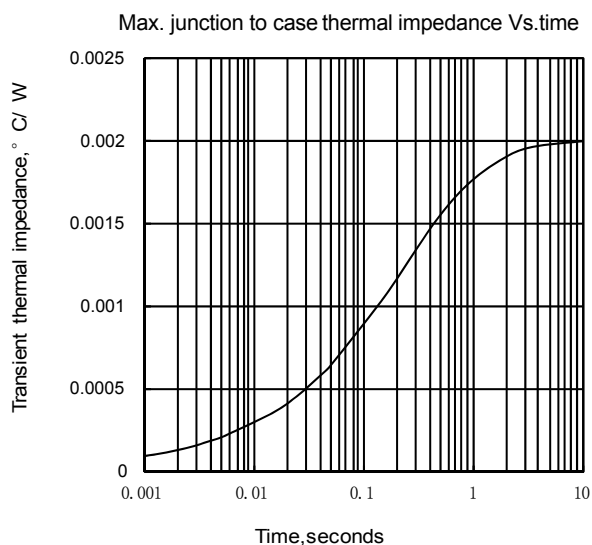


Fig.2

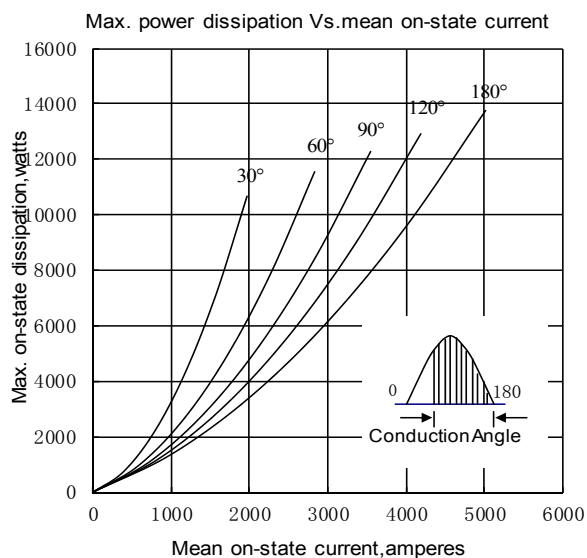


Fig.3

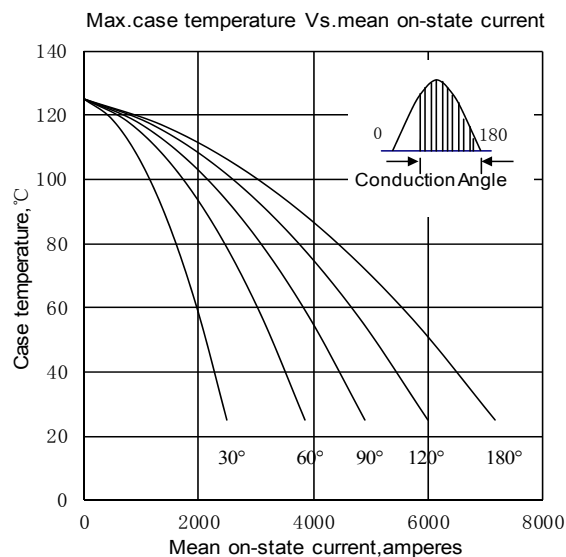


Fig.4

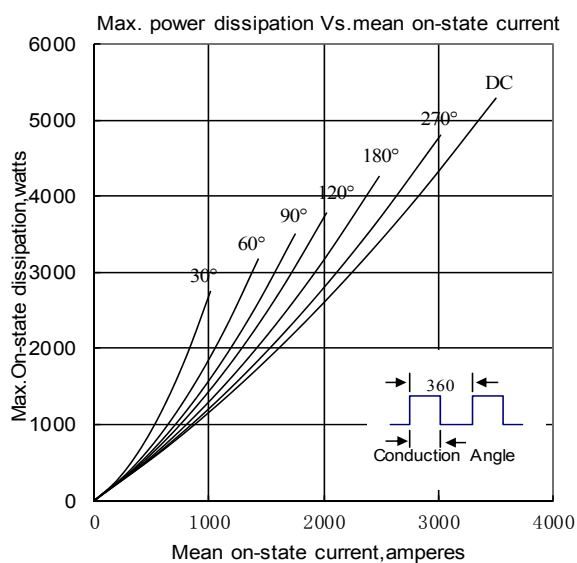


Fig.5

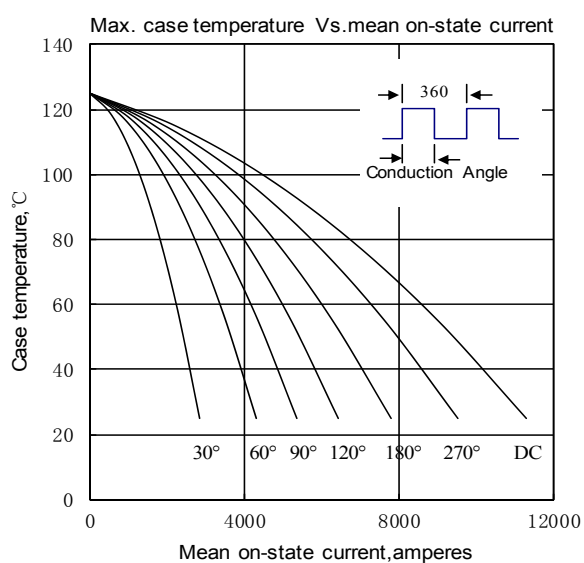


Fig.6

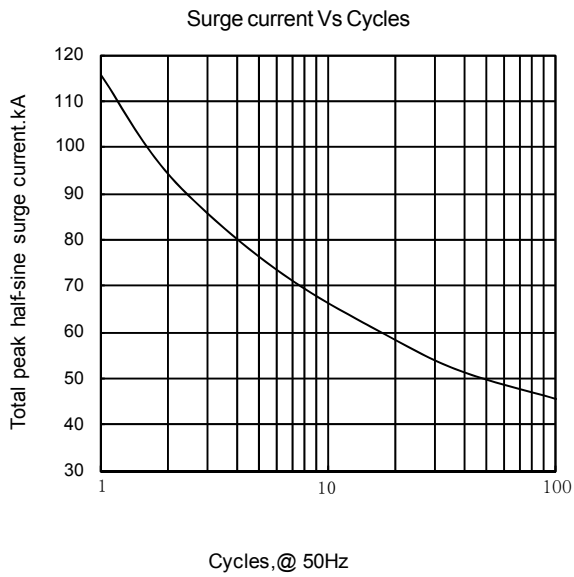


Fig.7

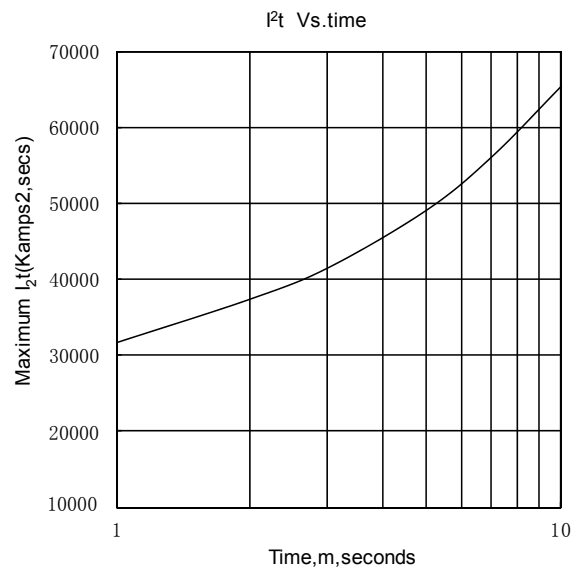


Fig.8

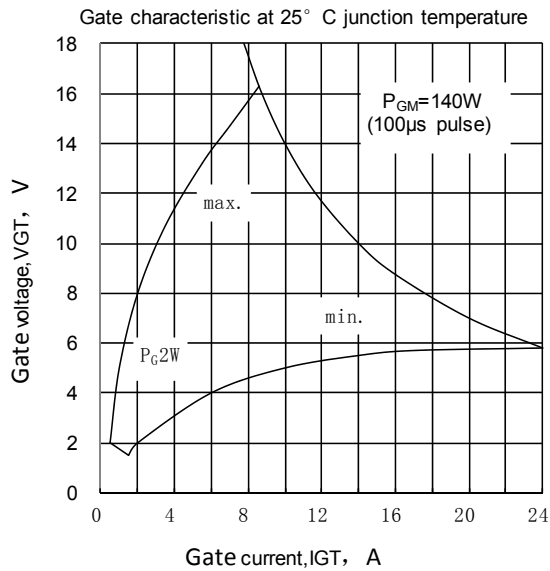


Fig.9

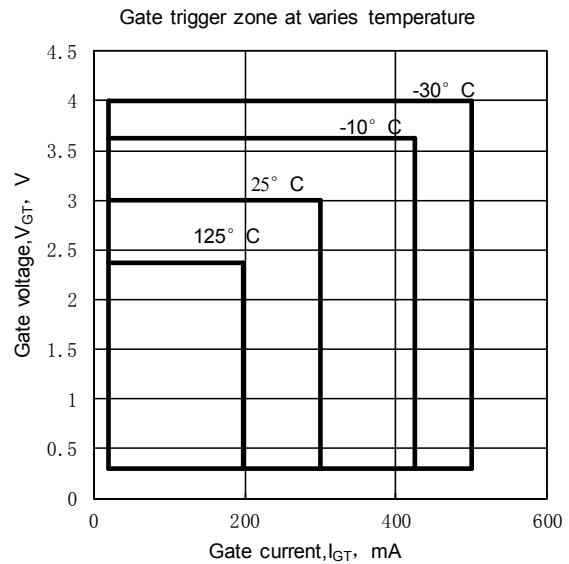


Fig.10

Outline:

