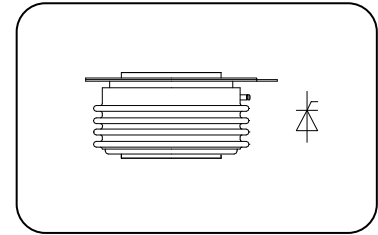




**Features:**

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses
- Short turn-off time
- Hermetic metal cases with ceramic insulators

$I_{T(AV)}$             **200A**  
 $V_{DRM}/V_{RRM}$     **800~1200V**  
 $t_q$                     **8~14μs**  
 $I_{TSM}$                 **5.4kA**



**Typical Applications**

- Inductive heating
- Electronic welders
- Self-commutated inverters
- AC motor speed control
- General power switching applications

SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T <sub>j</sub> (°C)	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T <sub>c</sub> =55°C	125		200	580	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ , tp=10ms $V_{DSM} \& V_{RSM} = V_{DRM} \& V_{RRM} + 100V$		125	800		1200	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak off-state current Repetitive peak reverse current	$V_D = V_{DRM}$ $V_R = V_{RRM}$		125			30	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave		125			5.4	kA
$I^2t$	I <sup>2</sup> T for fusing coordination	$V_R = 0.6V_{RRM}$					146	A <sup>2</sup> s*10 <sup>3</sup>
$V_{TO}$	Threshold voltage			125			1.45	V
$r_T$	On-state slop resistance						0.85	mΩ
$V_{TM}$	Peak on-state voltage	$I_{TM} = 600A$ , F=7.0kN		25			3.20	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM} = 0.67V_{DRM}$		125			1000	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM} = 67\%V_{DRM}$ , $I_{TM} = (2-3)I_{T(AV)}$ , t=5s Gate pulse t <sub>r</sub> ≤ 0.5μs I <sub>GM</sub> = 1.5A f=50Hz		125			600	A/μs
Q <sub>rr</sub>	Recovery charge	$I_{TM} = 500A$ , tp=1000μs, di/dt=-20A/μs, V <sub>R</sub> =100V		125		25	50	μC
t <sub>q</sub>	Circuit commutated turn-off time	$I_{TM} = 500A$ , tp=1000μs, V <sub>R</sub> = 100V dv/dt=30V/μs, di/dt=-20A/μs		125	8		14	μs
$I_{GT}$	Gate trigger current	$V_A = 12V$ , I <sub>A</sub> =1A		125	30		200	mA
$V_{GT}$	Gate trigger voltage				0.8		2.5	V
$I_H$	Holding current				20		250	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM} = 67\%V_{DRM}$		125			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 7.0kN					0.045	°C /W
$R_{th(c-h)}$	Thermal resistance case to heat sink						0.010	
F <sub>m</sub>	Mounting force				5.3		10	kN
T <sub>stg</sub>	Stored temperature				-40		140	°C
W <sub>i</sub>	Weight					85/195		g
Outline	KT30aT/KT36cT							

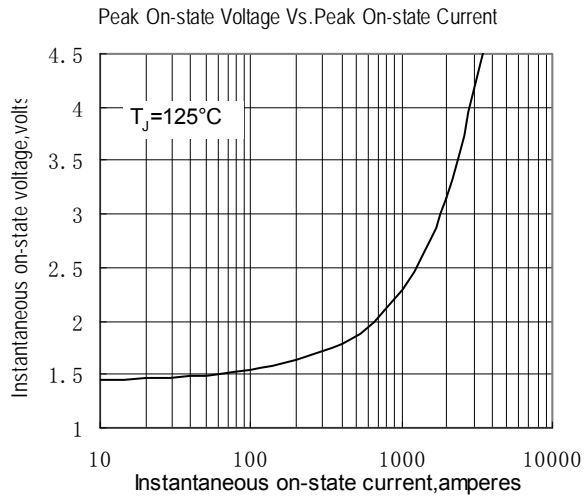


Fig.1

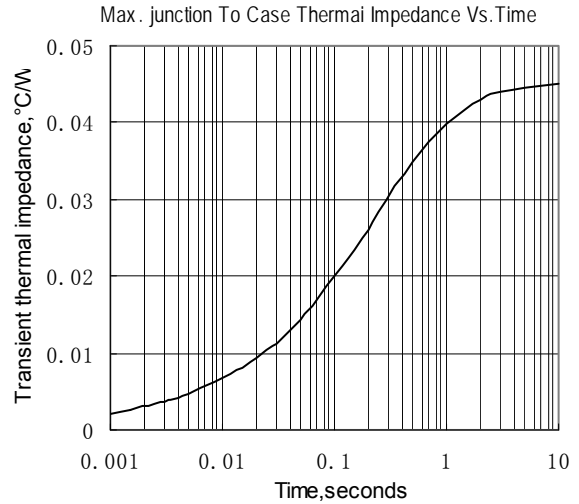


Fig.2

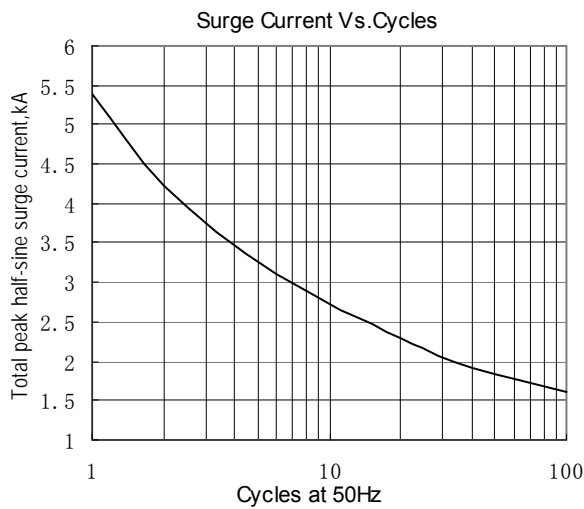


Fig.3

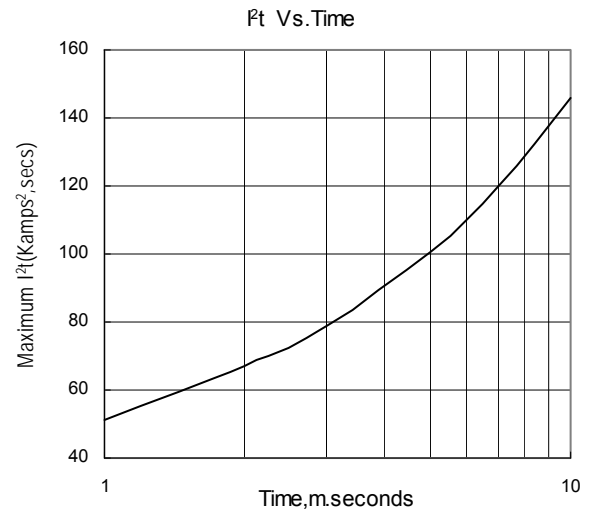


Fig.4

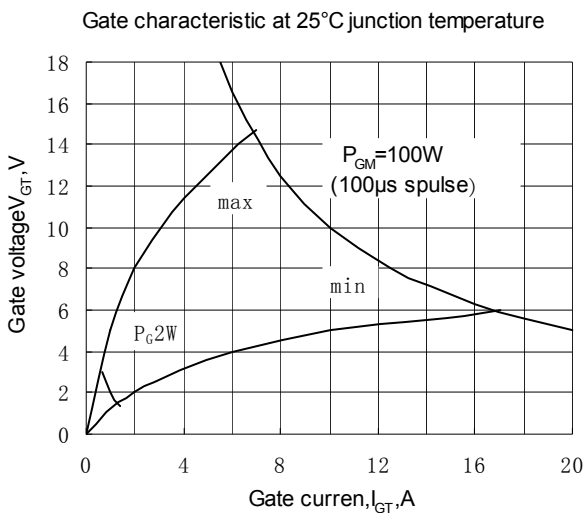


Fig.5

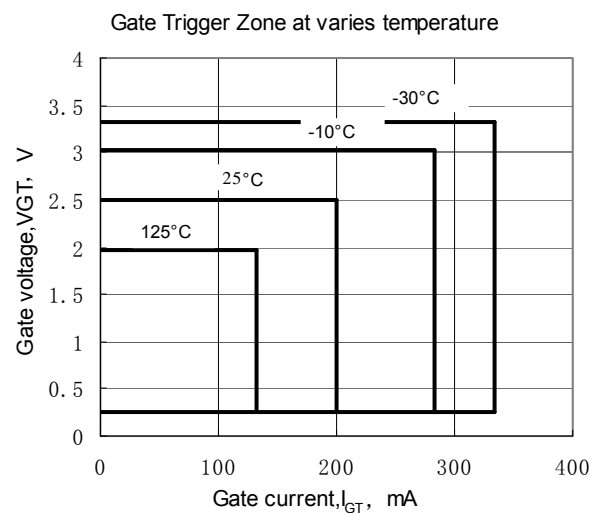


Fig.6



**Outline:**

图2-KT30aT

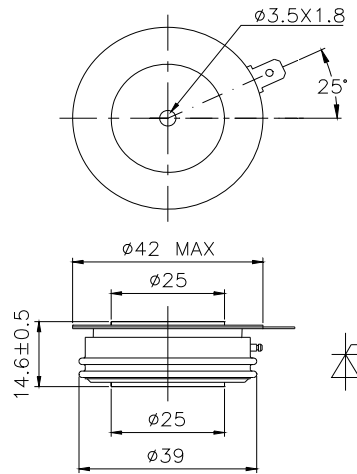
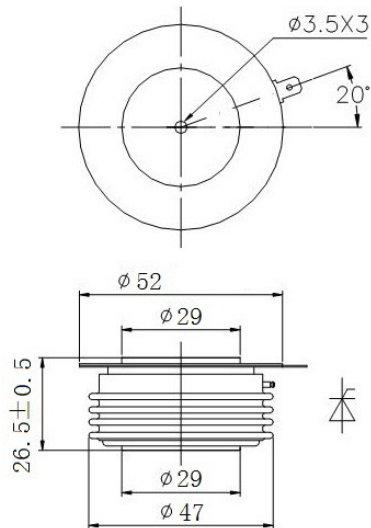


图3-KT36cT



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